

Jamares.Washington@gmail.com | [My Account](#) | [Sign out](#)[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

image processing during idle time, memory, id

[Search Patents](#)[Advanced Patent Search](#)
[Google Patent Search](#)

"of" is a very common word and was not included in your search. [\[details\]](#)

Patents Patents 1 - 10 on processing during idle time, memory, idle time controller controls alteration o

Cache memory controller and method for reducing CPU idle time by fetching data during a cache fill

US Pat. 5386526 - Filed Oct 18, 1991 - Sun Microsystems, Inc.

The 35 cache **memory controller** of the present invention can be practiced with ... from said cache and main 45 **memory** hierarchy that reduces CPU **idle time**, ...

Control sequencer with dual microprogram counters for microdiagnostics

US Pat. 4841434 - Filed Apr 27, 1987 - Raytheon Company

The **idle times** during which 5 background ... increasing said foreground program execution time; and control sequencer means coupled to said **memory** means for ...

System for enabling a CPU and an image processor to synchronously access a RAM ...

US Pat. 5844574 - Filed May 22, 1995 - Umax Data Systems, Inc.

It includes: image **processing** device 301, **memory** device 302, ... CPU 304 will then read the **data during the time segment** 403 from the CPU **data bus** 312. FIG. ...

Memory structure capable of preventing data loss therein and method for protecting the same

US Pat. 6459624 - Filed Sep 1, 2000 - Megawin Technology Co., Ltd.

Of course, when the system is under normal operation, the logical **controller** will use the **idle time** of the CPU to detect whether the **backup data** in the hard ...

Method for improving direct memory access performance

US Pat. 6484218 - Filed Oct 1, 1999 - Texas Instruments Incorporated

... the microprocessor **idle time** during **data transfers** for peripheral devices ... **bus controller** adapted to process requests from a host central **processing** ...

Extended range cordless telephone system

US Pat. 5243641 - Filed Jul 17, 1992 - AT&T Bell Laboratories

1. dates this information in temporary **memory** in its con- After ... all of **data** indicative of their respectively selected frequency 45 the **idle base units**. ...

Method and system for communication with an engine control module in sleep mode

US Pat. 5999876 - Filed Apr 1, 1998 - Cummins Engine Company, Inc.

In the next step of the sleep mode task algorithm, the 20 various volatile **memory** **data caches** are flushed to **flash memory** so that the volatile **memory** can be ...

Method and apparatus for dynamic pipelining

US Pat. 6643329 - Filed Aug 9, 2000 - NJR Corporation

In this case, the MS 708a the **controller** receives a **data valid** FALSE signal, and the completed reading **data** from the IDCT **memory** buffer at **controller** ...

Method and apparatus for I/O multiplexing of RAM bus

US Pat. 5761129 - Filed Mar 25, 1997 - Adaptec, Inc.

The RAM and I/O controller as recited in claim 4 wherein said clock signal provides a plurality of timing transitions during said idle state, and said logic ...

System and method for updating firmware in a non-volatile memory without using a processor
US Pat. 7043597 - Filed Sep 3, 2003 - MediaTek Inc.

During the Puter> the processing system comprising: loading period, the write command and the data for the a non-volatile memory (NVM) for storing firmware ...

Google ►

Result Page: 1 2 3 4 [Next](#)

[processing during idle time, memory, idle time](#) [Search Patents](#)

[Google Patent Search Help](#) | [Advanced Patent Search](#)

[Google Home](#) - [About Google](#) - [About Google Patent Search](#)

©2007 Google

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)[Sign in](#)**Google** [Search](#) [Advanced Search](#)[Preferences](#)**Web** Results 1 - 10 of about 1,080,000 for **processing during idle time after data is stored**. (0.14 seconds)**System for destaging data during idle time by transferring to ...**

If a read or a write request comes during destaging, the destaging process is suspended until the next **idle time** is found. After destaging, destage segment ...

www.freepatentsonline.com/5754888.html - 75k - [Cached](#) - [Similar pages](#)

Preemptable idle time activities for constant data delivery by ...

A method and apparatus for performing preemptable calibration and housekeeping functions **during idle time** between commands thereby maximizing **data** ...

www.freepatentsonline.com/5838991.html - 42k - [Cached](#) - [Similar pages](#)

[[More results from www.freepatentsonline.com](#)]

Preemptable idle time activities for constant data delivery by ...

While these housekeeping functions are performed (and the **data stored** on disk), ... **during idle time** between commands thereby maximizing **data** throughput. ...

www.patentstorm.us/patents/5838991-description.html - 32k - [Cached](#) - [Similar pages](#)

[PDF] Novel Data Acquisition System for Silicon Tracking Detectors

File Format: PDF/Adobe Acrobat - [View as HTML](#)

stored on a hard drive **during the idle** period while, waiting for new **data**. are executed in the **idle time after the processing** has ...

www.slac.stanford.edu/econf/C0303241/proc/papers/TUGP006.PDF - [Similar pages](#)

[PDF] arXiv:physics/0306015 v1 2 Jun 2003

File Format: PDF/Adobe Acrobat - [View as HTML](#)

size of saved **data during** one spill cycle amounts on. the average to 6.5 MB for 22800 events ... are executed in the **idle time after the processing** has ...

arxiv.org/pdf/physics/0306015 - [Similar pages](#)

EP572228 Canon european software patent - Data processing method ...

EP572228 Canon kk (JP): **Data processing** method and apparatus thereof. ... information stored in said memory means, **during process idle time** in and **after** the ... gauss.ffii.org/PatentView/EP572228 - 56k - [Cached](#) - [Similar pages](#)

Dynamic Load Balancer Plus

After all the samples have been taken and the I/O **data** has been collected: **Idle time** is the reserve capacity the system has to handle processes ...

www.ttinet.com/doc/dlbplus_clean_002.html - 30k - [Cached](#) - [Similar pages](#)

[PDF] Memory Space Representation for Heterogeneous Network Process ...

File Format: PDF/Adobe Acrobat

mean **idle-time** of the machines is sufficiently long for har- To support the **data** collection and. restoration **during process** migration, ...

ieeexplore.ieee.org/iel4/5485/14764/00670020.pdf?arnumber=670020 - [Similar pages](#)

FSIDataAndAnalysis < Atlas < TWiki

This solution does not seem to be very cost effective - the on-board processors would **idle** during the **time** when no FSI scans are taken. ...

<https://twiki.cern.ch/twiki/bin/view/Atlas/FSIDataAndAnalysis> - 29k - [Cached](#) - [Similar pages](#)

[PDF] [A Case Study in Quantitative Evaluation of Real-Time Architectures](#)

File Format: PDF/Adobe Acrobat

real-time processing. During the sample data processing the operator can actuate
the response time is influenced by the idle time of the timer task. ...

www.springerlink.com/index/0BQGKU0VPNB0FULF.pdf - [Similar pages](#)

1 [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

Download [Google Pack](#): free essential software for your PC

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [Gmail](#) [more ▾](#)Jamares.Washington@gmail.com | [My Account](#) | [Sign out](#)[Google](#)[Search](#)[Advanced Search](#)
[Preferences](#)[New!](#) [View and manage your web history](#)[Web](#)Results 1 - 10 of about 179,000 for **recompression of data during idle time**. (0.13 seconds)

SNL Parallel I/O - Salvo

I/O During Initialization. The reading of the trace **data** can be very expensive, especially when ... Although there is a lot of **idle time** on the I/O nodes, ...

www.cs.sandia.gov/~ccober/seismic/parallel_io.html - 7k -

[Cached](#) - [Similar pages](#) - [Note this](#)

Recompression of files at an intermediate node in a network system ...

The **recompression** server 20 also includes a **data** path from the web proxy server ... files and recompress them **during idle time** for the web proxy server 10'. ...

www.patentstorm.us/patents/6112250-description.html - 30k -

[Cached](#) - [Similar pages](#) - [Note this](#)

Automatic electronic **data** type identification process - US Patent ...

This encoded information is recovered **during data decompression** to notify the ... as to provide normal rate of **data** compression **during the CPU's idle time**, ...

www.patentstorm.us/patents/5374916-description.html - 39k -

[Cached](#) - [Similar pages](#) - [Note this](#)

[More results from www.patentstorm.us]

Recompression of data in memory - Patent 20020039483

The aim of the invention is to allow the **recompression of data** relating, ... a PVR/broadcast **data** receiver/DVD player will be recompressed **during times** when ...

www.freepatentsonline.com/20020039483.html - 27k - [Cached](#) - [Similar pages](#) - [Note this](#)

Visual display system having low energy **data** storage subsystem ...

The **data** is compressed by a microprocessor **during idle times** and decompressed by a **decompression** circuit on the fly. In some circumstances, the power ...

www.freepatentsonline.com/5512921.html - 67k - [Cached](#) - [Similar pages](#) - [Note this](#)

[More results from www.freepatentsonline.com]

Date: Sun, 16 May 1993 14:00:10 -0500 From: tonyh@msc.cornell.edu ...

On the down side, if the system crashes you risk losing **data** still in the cache. The cache is flushed **during idle time** or when full. ...

hyperarchive.lcs.mit.edu/HyperArchive/Archive/cmp/driver-level-compression.txt - 18k -

[Cached](#) - [Similar pages](#) - [Note this](#)

NetWare 4 Compression: The Incredible Shrinking **Data**

Since a compressed file is a bit map of **data**, NetWare 4 interprets a compressed file one bit at a **time during the decompression** process. ...

support.novell.com/techcenter/articles/nc1998_02c.html - 38k -

[Cached](#) - [Similar pages](#) - [Note this](#)

Virtual Memory for a Handheld Computer

Compression and **decompression** of **data** occurs at an average of 4 energy used **during access time** to energy used **during any remaining idle time** for both ...

web.mit.edu/6.033/2000/www/reports/dp1-kenlu/ - 27k - [Cached](#) - [Similar pages](#) - [Note this](#)

[PDF] Time-Multiplexed Test **Data** Decompression Architecture for Core ...

File Format: PDF/Adobe Acrobat

Time-Multiplexed Test Data Decompression Architecture for channels are **idle** during un-seeded blocks. When packing is used ...
ieeexplore.ieee.org/iel5/9778/30844/01430030.pdf?arnumber=1430030 -
[Similar pages](#) - [Note this](#)

[PDF] [A unified SoC test approach based on test data compression and TAM ...](#)

File Format: PDF/Adobe Acrobat

ner in which on-chip test data **decompression** is implemented. **Idle time**. 217730.
203448. Figure 12. New test schedule for SOC ...
ieeexplore.ieee.org/iel5/8839/27973/01250150.pdf - [Similar pages](#) - [Note this](#)
[[More results from ieeexplore.ieee.org](#)]

1 2 3 4 5 6 7 8 9 **10** [Next](#)

Download [Google Pack](#): free essential software for your PC

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

.....
©2007 Google - [Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)